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## BIB DATA SHEET

CONFIRMATION NO. 2837

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**APPLICANTS**  
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**\*\* CONTINUING DATA \*\*\*\*\***  
 This application is a 371 of PCT/JP04/16174 10/25/2004

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***  
 JAPAN 2003-373514 10/31/2003

**\*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\***  
 02/28/2007

Foreign Priority claimed 35 USC 119(a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No /MAGID Y DIMYAN/ Examiner's Signature	<input type="checkbox"/> Met after Allowance Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 8	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 3
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 UNITED STATES

**TITLE**  
 Semiconductor integrated circuit and design method thereof

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